

### REMARKS

Applicants respectfully request amendment to the instant application. The specification has been amended in the Disclosure of Invention section to correct a mistranslation therein. In the original specification, power supply lines in the Disclosure of Invention section are referred to as column and series, but in paragraphs [0158] – [0260], describing Figs. 9-12, Embodiments 2-5, the power supply lines are referred to as column and row. Thus, the amendment of the Disclosure of Invention section brings the language of paragraphs [0063] and [0064] in conformity with the language of paragraphs [0158] – [0260]. No new matter is added by this change.

More specifically, Embodiment 2 is described in paragraphs [0158] – [0180]. The power supply lines are initially described with reference to Fig. 9 in paragraph [0160] as “[a] pixel of row one and column one of the pixel portion includes the gate signal line G1, the source signal line S1, a power supply line Vx1, a power supply line Vy1, the switching TFT 1001, the driving TFT 1002, the EL element 1003, and the capacitor 1004.” Additionally, the embodiment is further described in paragraph [0168] as “power supply lines in the pixel portion are not only the wirings (vx1 to Vxn) provided in parallel with the source signal lines (S1 to Sn) but also provided in a vertical direction or an almost vertical direction (Vy1 to Vym), thereby voltage is supplied from each direction to the source region or the drain region of the driving TFT 1002 of the pixel. The power supply lines provided in a vertical direction or an almost vertical direction (Vy1 to Vym) are connected to the power supply lines (Vx1 to Vxn) per pixel and the power supply lines are arranged in matrix. Accordingly, a current flowing through the EL element 1003 is supplied not only in a direction parallel to the source signal lines (S1 to Sn) but also in the vertical direction.” Embodiments 3-5 are similarly described in paragraphs [0183], [0210], and [0243]. Each of these paragraphs use the language “row” and “column” to describe pixels and power supply lines. While the original Disclosure of Invention section uses the language “column” and “series” to describe power supply lines in paragraphs [0063] and [0064], the Best Mode for Carrying out the Invention section consistently describes the

power supply lines using the language “row” and “column”. Therefore, the present amendment brings the inconsistent language in conformity.

Additionally, Applicants have amended claims 1, 2, and 4 to reflect the correction of the mistranslation, supported in the specification in Figs. 9-12 and paragraphs [0158] – [0260].

Examination on the merits is requested.

Respectfully submitted,

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